

FIG. 1A

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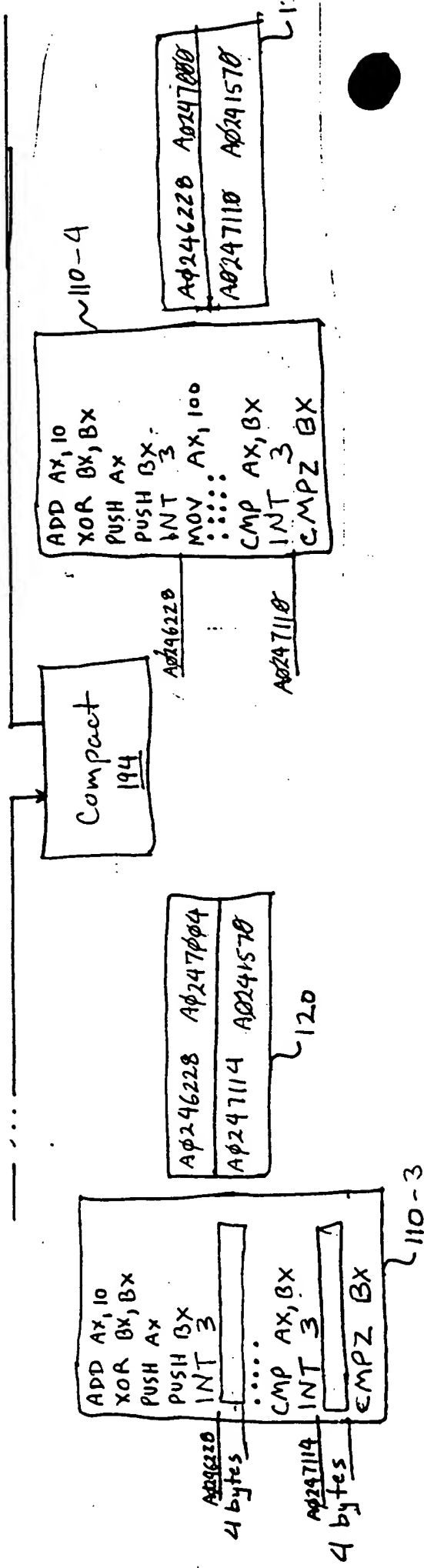


FIG. 1B

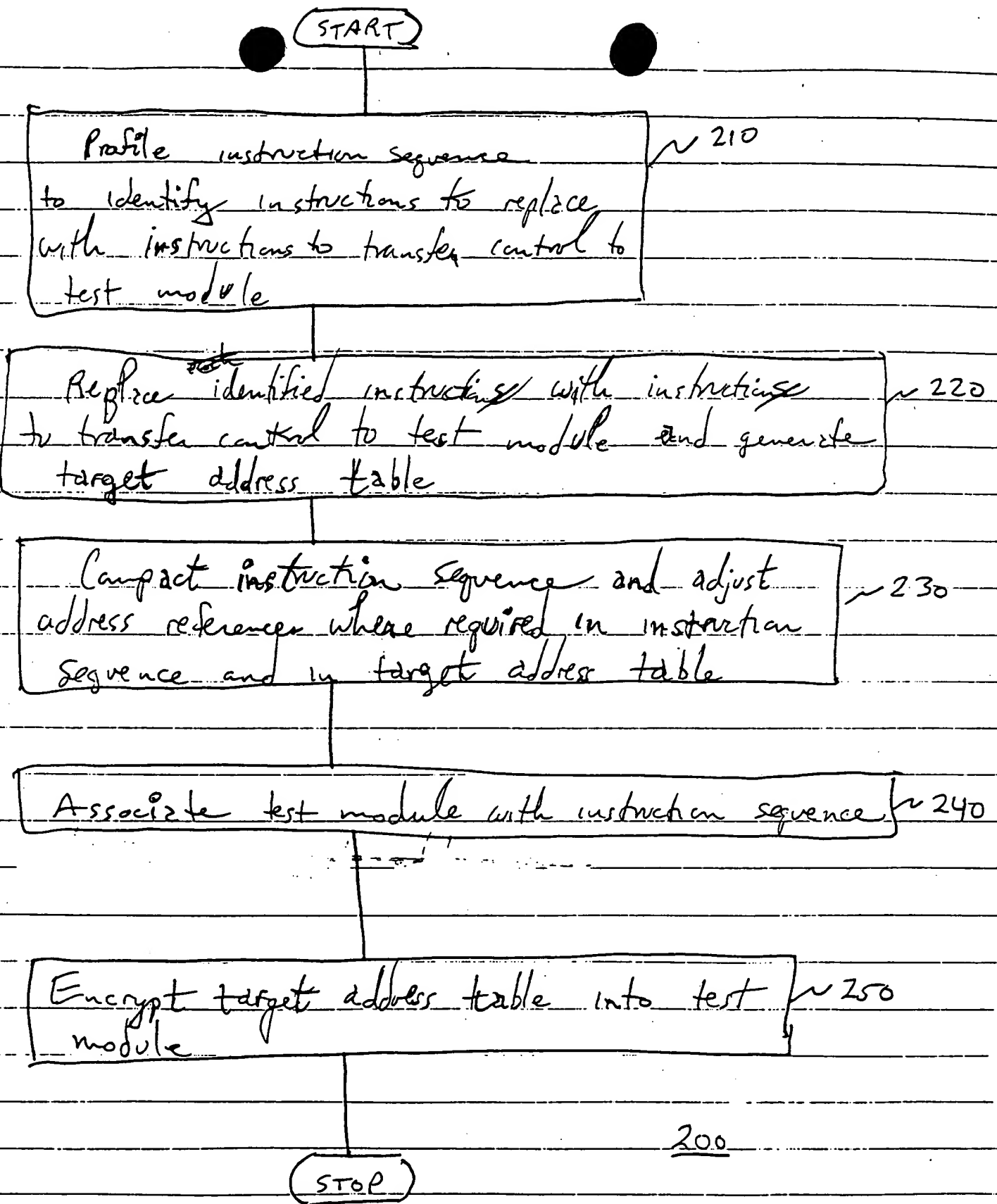


FIG. 2

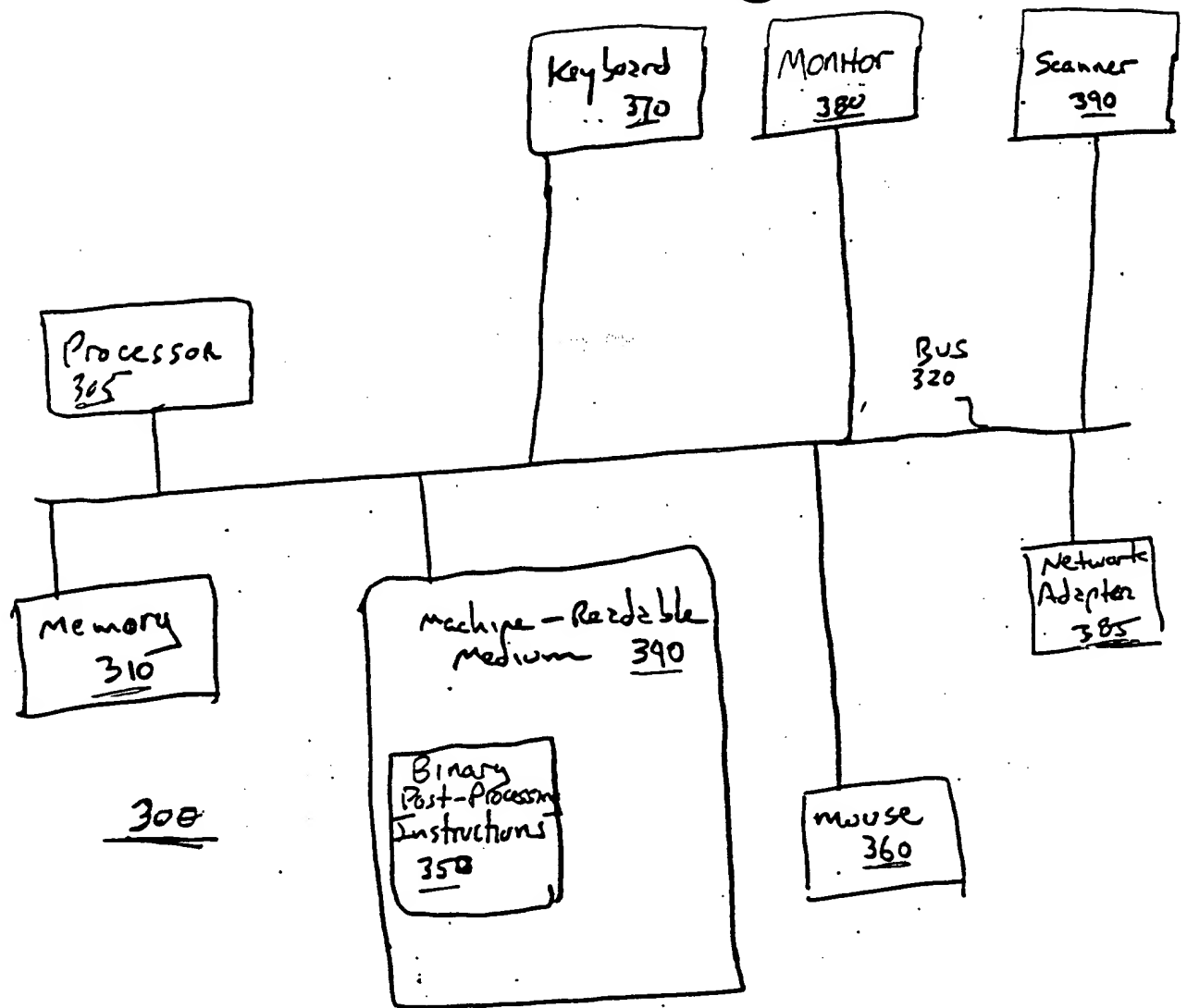


FIG. 3

Machine-Readable
Storage Medium

400

Binary
Post-Processing
Instructions
410

420 Instructions to identify instructions in instruction sequence to replace

430 Instructions to replace identified instructions with instructions to transfer control to test module

440 Instructions to generate target address table

450 Instructions to compact instruction sequence and adjust address references in instruction sequence and target address table

460 Instructions to associate test module with instruction sequence

470 Instructions to Encrypt target address table into test module

FIG. 5

Machine-Readable
Storage Medium
500

